

7	8
REV.	ECN NO.
REVISED	DATE

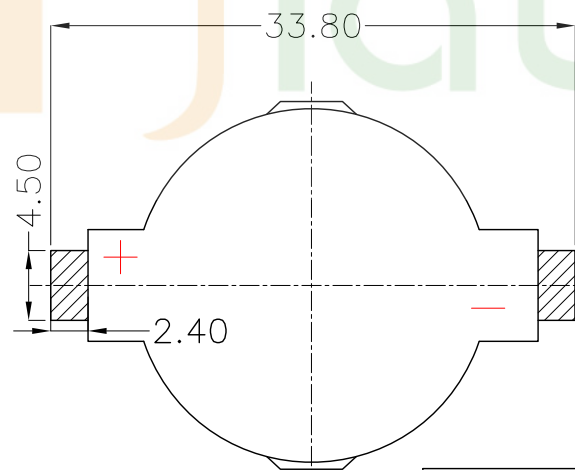
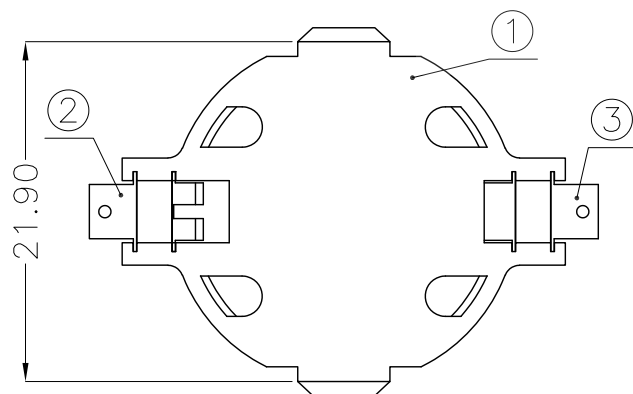
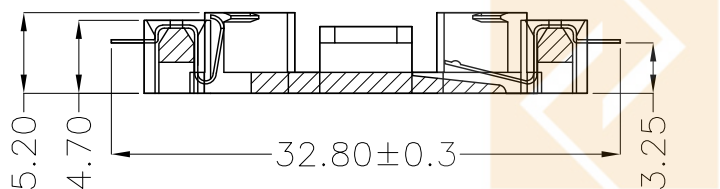
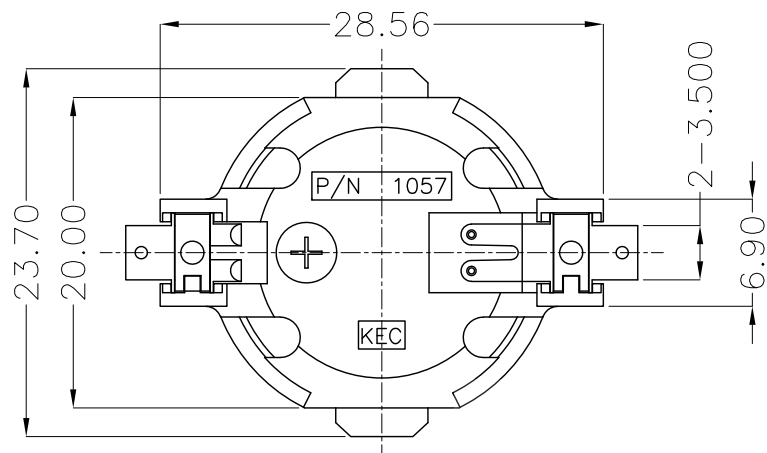
NOTE:

1.MATERIAL:

- a.HOUSING:LCP UL94V-0
b.CONTACT:PHOSPHOR BRONZE
(SURFACE PLATING:Sn=100u"-120u")
SEE ORDERING INFORMATION

2.ELECTRIC:

- a.CONTACT RESISTANCE: 30 MILLIOHMS MAX
b.INSULATION RESISTANCE: 1000 MEGA OHMS MIN
c.DIELECTRIC VOLTAGE: 500V AC ONE SEC LEVEL
d.OPERATING TEMPERATURE: -25°C~ +85°C



PCB Layout Diagram
Top View

3	TERMINAL(-)	1	PHOSPHOR BRONZE, T=0.20mm	Gold Plating and Ni Under-Plating
2	TERMINAL(+)	1	PHOSPHOR BRONZE, T=0.20mm	Gold Plating and Ni Under-Plating
1	HOUSING	1	LCP	WHITE
NO.	PART NAME	Q'TY	MATERIAL	PLATING & COLOR

UNLESS OTHERWISE SPECIFIED TOLERANCES		Shenzhen Jiatel Hardware Electronics Co.,Ltd www.jiatel.com			
DECIMALS:	ANGLES:	DSND	Wei Peng	DATE	2017-07-16
X :±0.30	X :±1°	CHKD	Fang Cui	DATE	2017-07-16
X.X :±0.25	X.X :±0.5°	APVD	Eric Zhang	DATE	2017-07-16
X.XX :±0.20					
X.XXX :±0.10					
WEIGHT: 1.45g		SCALE: 2:1	SIZE: A4	VIEW:	
SHEET 1 OF 1		UNIT: mm	REV.: A/0	OBJECT: JT	

TITLE:
2032-12 Au SMT
PART NO.:
JT-BH2032-12
DRAW NO.:JT-CP-0092