

7	8
REV.	ECN NO.
REVISED	DATE

NOTE:

1.MATERIAL:

a.HOUSING:PBT

b.CONTACT:PHOSPHOR BRONZE

(SURFACE PLATING: Ni100u"—120u")

SEE ORDERING INFORMATION

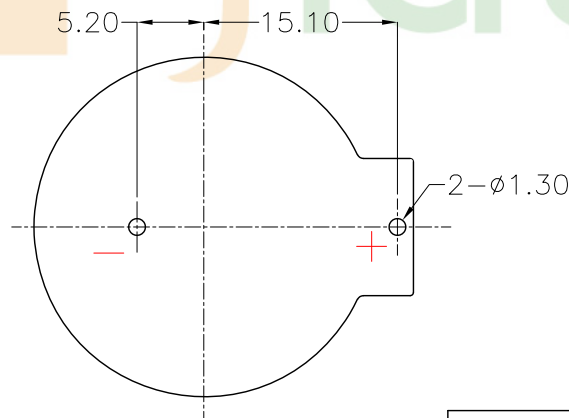
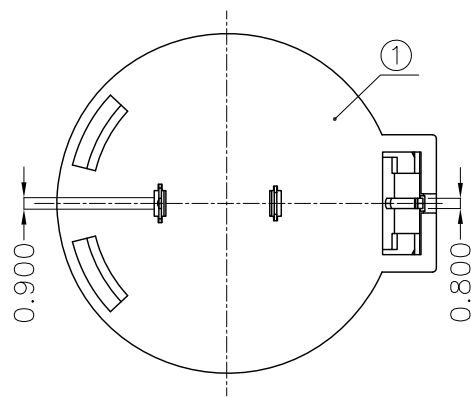
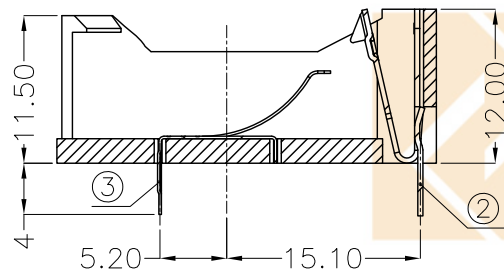
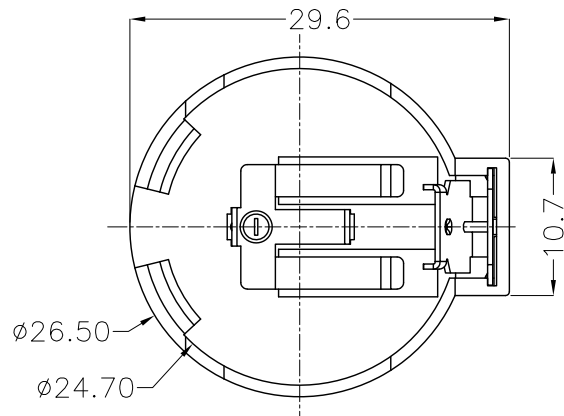
2.ELECTRIC:

a.CONTACT RESISTANCE: 30 MILLIOHMS MAX

b.INSULATION RESISTANCE: 1000 MEGA OHMS MIN

c.DIELECTNIC VOLTAGE: 500V AC ONE SEC LEVEL

d.OPERATING TEMPERATURE: -25℃~ +85℃



PCB Layout Diagram
Top View

3	TERMINAL(-)	1	PHOSPHOR BRONZE,T=0.20mm	Ni Plating
2	TERMINAL(+)	1	PHOSPHOR BRONZE,T=0.40mm	Ni Plating
1	HOUSING	1	PBT	BLACK
NO.	PART NAME	Q'TY	MATERIAL	PLATING & COLOR

UNLESS OTHERWISE SPECIFIED TOLERANCES		Shenzhen Jiatel Hardware Electronics Co.,Ltd www.jiatel.com					
DECIMALS:	ANGLES:	DSND	Wei Peng	DATE	2017-07-17	TITLE:	
X :±0.30	X :±1°	CHKD	Fang Cui	DATE	2017-07-17	CR2477-1 DIP	
X.X :±0.25	X.X :±0.5°	APVD	Eric Zhang	DATE	2017-07-17	PART NO.:	
X.XX :±0.20						JT-BH2477THM	
X.XXX :±0.10						DRAW NO.:JT-CP-0039	
WEIGHT: 3.2g		SCALE: 2:1	SIZE: A4	VIEW:			
SHEET 1 OF 1		UNIT: mm	REV.: A/0	OBJECT: JT			