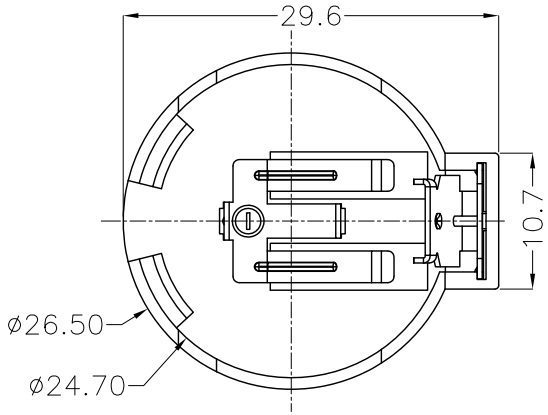
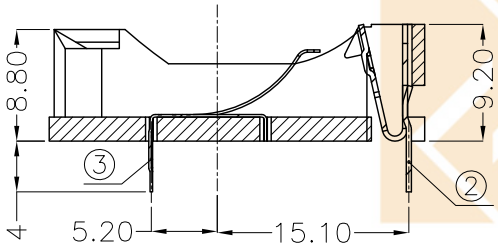
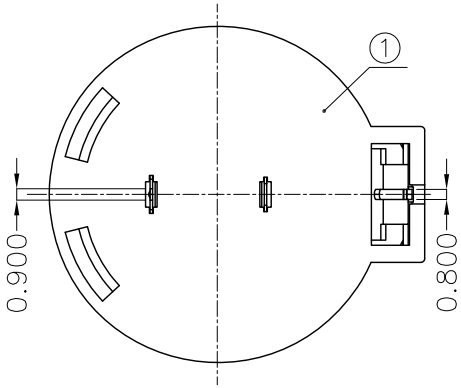
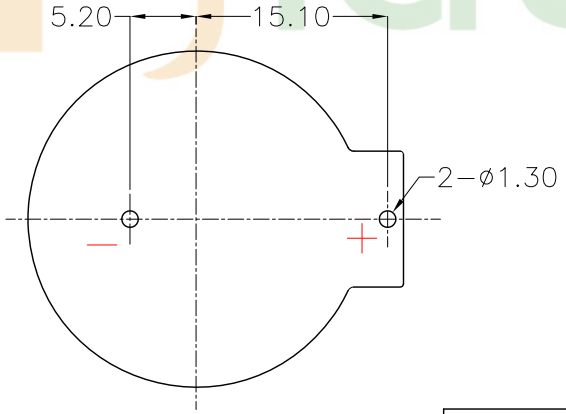
REV.	ECN NO.	REVISED	DATE









PCB Layout Diagram
Top View

NOTE:
1.MATERIAL:
a.HOUSING:PBT
b.CONTACT:PHOSPHOR BRONZE
(SURFACE PLATING: Ni100u"-120u")
SEE ORDERING INFORMATION
2.ELECTRIC:
a.CONTACT RESISTANCE: 30 MILLIOHMS MAX
b.INSULATION RESISTANCE: 1000 MEGA OHMS MIN
c.DIELECTNIC VOLTAGE: 500V AC ONE SEC LEVEL
d.OPERATING TEMPERATURE: -25℃~ +85℃

NO.	PART NAME	Q'TY	MATERIAL	PLATING & COLOR
3	TERMINAL(-)	1	PHOSPHOR BRONZE,T=0.20mm	Ni Plating
2	TERMINAL(+)	1	PHOSPHOR BRONZE,T=0.40mm	Ni Plating
1	HOUSING	1	PBT	BLACK

UNLESS OTHERWISE SPECIFIED TOLERANCES	Shenzhen Jiatel Hardware Electronics Co.,Ltd www.jiatel.com				
DECIMALS: X :±0.30 X.X :±0.25 X.XX :±0.20 X.XXX :±0.10 ANGLES: X :±1° X.X :±0.5°	DSND	WeiPeng	DATE	2017-07-15	TITLE: 2450-1 DIP
	CHKD	Eric Zhang	DATE	2017-07-15	
	APVD	Fang Cui	DATE	2017-07-15	PART NO.: JT-BH2450-1
WEIGHT:2.71g	SCALE: 2:1	SIZE: A4	VIEW:		
SHEET 1 OF 1	UNIT: mm	REV.: A/0	OBJECT: JT		DRAW NO.:JT-CP-0037